

```
graph TD
    1[1 AMORPHOUS SILICON FILM FORMATION STEP] --> 2[2 SPC STEP]
    2 --> 3[3 LASER PROCESSING STEP]
    3 --> 5[5 GATE INSULATION FILM FORMATION STEP]
    5 --> 4[4 OPTIMUM POWER INSPECTION/EXTRACTION STEP]
    4 --> 1
```

The flowchart illustrates the manufacturing process for an extracting monitor substrate. It begins with Step 1: AMORPHOUS SILICON FILM FORMATION STEP. This is followed by Step 2: SPC STEP. Then, Step 3: LASER PROCESSING STEP is performed. After Step 3, the process moves to Step 5: GATE INSULATION FILM FORMATION STEP. Finally, Step 4: OPTIMUM POWER INSPECTION/EXTRACTION STEP is conducted. A feedback loop connects Step 4 back to Step 3, labeled 'HAVING UNDERGONE LASER PROCESSING' and 'SETTING LASER POWER'. The entire process is labeled 'EXTRACTING MONITOR SUBSTRATE'.

FIG.9

Prior Art

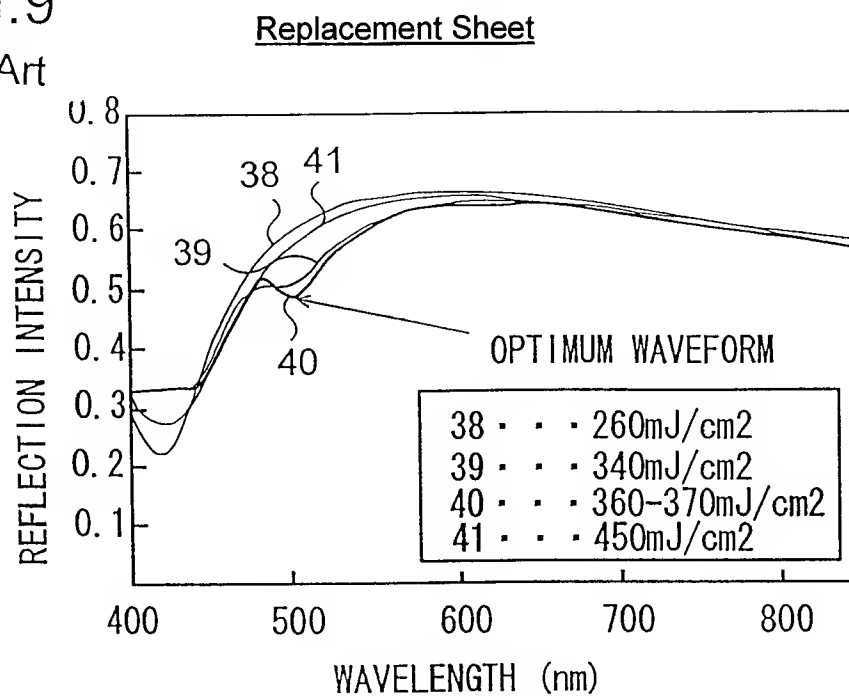


FIG.10

